WHAT IS CLAIMED

1. An integrator circuit comprising:

an input port;

an output port;

an operational amplifier having an input terminal coupled through an input resistor to said input port and having an output terminal coupled to said output port;

a capacitor and a first output resistor coupled in series between said input terminal and said output terminal of said operational amplifier; and

a second output resistor coupled between a reference potential terminal and a common connection of said capacitor and said first output resistor.

2. The integrator circuit according to claim 1, wherein at least one of said first and second output resistors is adjustable.

3. The integrator circuit according to claim 1, wherein at each of said first and second output resistors is adjustable.

4. An integrator circuit comprising:

an operational amplifier having an inverting input coupled to an input resistor to which an input voltage is supplied, a non-inverting input coupled to a reference potential, and an output from which an output voltage is derived;

a capacitor and a first output resistor coupled in series between said inverting input and said output of said operational amplifier; and

a second output resistor coupled between said reference potential and a common connection of said capacitor and said first output resistor.

5. The integrator circuit according to claim 4, wherein at least one of said first and second output resistors is adjustable.

6. The integrator circuit according to claim 4, wherein at each of said first and second output resistors is adjustable.